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2827

November 19, 2003

Mail Stop Non-Fee Amendment
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: U.S. Patent Application No. 09/683,303

Title: Flip-Chip Packaging Method That Treats an Interconnect
Substrate to Control Stress Created at Edges of Fill Material (As
Amended)
Inventors: Robert M. Hilton and Sabran Bin Samsuri
Filing Date: December 11, 2001
Examiner: Mitchell, James M. Group Art Unit: 2827
Docket No.: MTB001US

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

1. Return Receipt Postcard;
2. This Transmittal Letter (in duplicate); and
3. Response to Office Action (5 pages).

Conditional Petition for Extension of Time

If after the document(s) filed with this transmittal have been considered, an extension of time is required for timely filing of the document(s), an extension of time is hereby requested.

Fees

- ☒ No additional fee is required.
- ☒ Please charge any additional fees required and credit any overpayment to Deposit Account No. 50-1767.

EXPRESS MAIL LABEL NO.

ER 527 529 489 US

Respectfully submitted,

David T. Millers

David T. Millers
Reg. No. 37,396

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Robert M. Hilton and Sabran Bin Samsuri
Assignees: Celerity Research Pte. Ltd. and ASE Electronics (M) Sdn. Bhd.
Title: Flip-Chip Packaging Method that Treats an Interconnect Substrate to Control Stress Created at Edges of Fill Material (As Amended)
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RESPONSE TO OFFICE ACTION

Dear Sir:

This response to the Office Action dated August 19, 2003, which has a statutorily shortened period for response that ends November 19, 2003, includes:

- An amendment to the specification or abstract;
- A listing of the claims presented for further examination; and
- Remarks given in reply to the Office Action dated August 19, 2003.

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